

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of

Applicant : David L. Chapek

Serial No. : 09/605,293

Filed : June 28, 2000

Title : SEMICONDUCTOR DEVICES INCLUDING A LAYER OF TECHNOLOGY CENTER 2800
POLYCRYSTALLINE SILICON HAVING A SMOOTH MORPHOLOGY

Docket : MIO 0037 VA

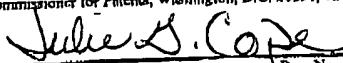
Art Unit : 2811

Assistant Commissioner for Patents
Washington, DC 20231

Sir:

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JAN 9 2002

CERTIFICATE OF MAILING
 I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on November 15, 2001.

 Julie G. Cope - Attorney Reg. No. 48,624

AMENDMENT

This paper is being filed in response to the Office Action mailed on October 2, 2001. Reconsideration is respectfully requested in light of the remarks below.

CLEAN VERSION OF THE AMENDMENTS

(A version of these amendments with markings to show the changes is attached to this paper as a separate appendix.)

IN THE CLAIMS

9. (Amended) A semiconductor device precursor comprising:

a semiconductor substrate;

a layer of silicon dioxide formed on said semiconductor substrate, said layer of silicon dioxide having been doped with hydrogen ions deposited by a plasma source ion implantation process, wherein said layer of silicon dioxide is free of metal contaminants; and

a layer of polycrystalline silicon formed on said layer of silicon dioxide, said layer of polycrystalline silicon having a smooth morphology.

10. (Amended) A field effect transistor comprising:

FACSIMILE COVER SHEET

TO: N. Drew Richards

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FROM: Julie G. Cope

Date: January 9, 2002
Pages: 12
(including cover page)
Our Docket No.: MIO 0037 VA
Transmission No.: _____

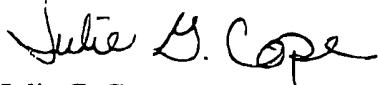
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Remarks:

Mr. Richards –

This is a copy of an amendment that was mailed to the USPTO on the date of the certificate of mailing.

Thank you –



Julie G. Cope
Registration No. 48,624

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